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JUL U 8 1998 GROUP 2100

Serial No.

Atty Docket No.

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PTO-FB-A820				Junichi KONNO et al			
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				August 21	. 1991		
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	AI	1-239933	2/01/89 9/25/89		apan	XX	
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IN THE UNITED STATES PATENT AND TRADEMARK

In re the application of:

ONNO et al

Filed:

erial No.: 07/743,383

Group Art Unit: August 21, 1991

Examiner:

METHOD FOR PRODUCING SEMICONDUCTOR INTEGRATED CIRCUITS AND For:

APPARATUS USED IN SUCH METHOD

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

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January 7, 1993

G. Goudreau

Sir:

GROUP 2100

In compliance with applicants duty of disclosure under 37 CFR 1.56, the following is material information of which applicants are aware and was cited in the attached European Search Report.

European Patent Appln. No. 0 416 774 dated 3/13/91

Solid State Technology, v. 33, no. 2 (Feb. 1990) 2.

Extended Abstracts, vol. 81-2 (1981) pp. 715-716

Japanese Patent Abstract of JP-A-1 251 742 (Jan. 1989) 4. European Patent Appln. No. 0 345 757 dated 12/13/89 5.

European Patent Appln. No. 0 387 097 dated 9/12/90

Commissioner is hereby authorized to charge the necessary fee of \$200.00 to Deposit Account No. 14-1060. A duplicate of this sheet is attached herewith.

In the event that any additional fees are due with this paper, please charge Counsel's Deposit Account No. 14-1060.

Respectfully submitted,

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Atty. Case No. P698-1333

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Enclosures: Information Disclosure Citation, European Search Report and References (6)



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Yes/No/Partial AL0 416 774 3/13/91 Europe Yes 0 345 757 AM 12/13/90 Europe Yes 0 387 097 AN 9/12/90 Europe Yes AO AP

OTHER DOCUMENTS AR Solid State Technology, vol. 33, no. 2 (February 1990) AS Extended Abstracts, vol. 81-2 (1981) pp. 715-716 AT Japanese Patent Abstract of JP-A-1 251 742 (January 1989)

Examiner Date Considered